

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S3	2	"6597656".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:30
S4	15534	(parallel near switch\$5) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:31
S5	1100	(parallel near switch\$5).ti. and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:31
S6	6	(parallel near switch\$5).ti. and ((ingress or input) adj (module or adapter or unit or card)) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:33
S7	11	(parallel near switch\$5).ti. and ((ingress or input) near (module or adapter or unit or card)) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:34
S8	43	(parallel near switch\$5).ti. and ((ingress or input) with (module or adapter or unit or card)) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:38
S9	31	(parallel near switch\$5).ti. and ((ingress or input) with (module or adapter or unit or card)) and ((egress or output) with (module or adapter or unit or card)) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:38

EAST Search History

S11	5	(parallel near switch\$5).ti. and ((ingress or input) with (module or adapter or unit or card)) and ((egress or output) with (module or adapter or unit or card)) and (priority) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:47
S12	206	(parallel adj switch\$5) and ((ingress or input) with (module or adapter or unit or card)) and ((egress or output) with (module or adapter or unit or card)) and (priority) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:50
S13	2	(parallel adj switch\$5) and ((ingress or input) with (module or adapter or unit or card)) and ((egress or output) with (module or adapter or unit or card)) and (priority) and (internal near (header or preamble or descriptor)) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 12:57
S14	13	(parallel adj switch\$5) and ((ingress or input) with (module or adapter or unit or card)) and ((egress or output) with (module or adapter or unit or card)) and (priority) and (internal with (header or preamble or descriptor)) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 15:19
S18	14	(parallel adj switch\$5) and ((ingress or input)) and ((egress or output)) and (priority) and ((time adj2 stamp) same (header or preamble or descriptor)) and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 15:30
S19	18	(parallel adj switch\$5) and ((ingress or input)) and ((egress or output)) and ((time adj2 stamp) same (header or preamble or descriptor)) and sequenc\$6 and (@rlad<"20021219" or @ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/21 15:32



PALM INTRANET

Inventor Information for 10/722901

Inventor Name	City	State/Country
BLANC, ALAIN	TOURRETTES SUR LOOP	FRANCE
GLAISE, RENE	NICE	FRANCE
PORET, MICHEL	GATTIERES	FRANCE
WIND, DANIEL	DRAP	FRANCE

- [Appln Info](#)
- [Contents](#)
- [Petition Info](#)
- [Atty/Agent Info](#)
- [Continuity/Reexam](#)
- [Foreign E](#)

Search Another: Application #

or Patent#

PCT / /

or PG PUBS #

Attorney Docket #

Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)



Welcome United States Patent and Trademark Office

 Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "(((parallel<in>metadata) <and> (switching<in>metadata))<and> ((sequen..."

Your search matched 583 of 1632036 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

 e-mail

» Search Options

[View Session History](#)
[New Search](#)

» Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

 Check to search only within this results set

 Display Format: Citation Citation & Abstract

 [Select All](#) [Deselect All](#)

View: 1-25 | :

1. **Analysis of out-of-sequence problem and preventative schemes in parallel switch architecture ATM network**
 Jung, Y.C.; Un, C.K.; Ryu, S.M.; Lee, S.C.;
[Communications, IEE Proceedings-](#)
 Volume 141, [Issue 1](#), Feb. 1994 Page(s):29 - 38
[AbstractPlus](#) | Full Text: [PDF\(652 KB\)](#) IET JNL
2. **An O(log/sup 2/ N) parallel algorithm for output queuing**
 Prakash, A.; Sharif, S.; Aziz, A.;
[INFOCOM 2002, Twenty-First Annual Joint Conference of the IEEE Computer and Communication Proceedings, IEEE](#)
 Volume 3, 23-27 June 2002 Page(s):1623 - 1629 vol.3
 Digital Object Identifier 10.1109/INFCOM.2002.1019415
[AbstractPlus](#) | Full Text: [PDF\(269 KB\)](#) IEEE CNF
[Rights and Permissions](#)
3. **A novel modeling and control approach for parallel three-phase buck rectifiers**
 Zhihong Ye; Boroyevich, D.;
[Industry Applications Conference, 2001. Thirty-Sixth IAS Annual Meeting, Conference Record of the](#)
 Volume 1, 30 Sept.-4 Oct. 2001 Page(s):350 - 356 vol.1
 Digital Object Identifier 10.1109/IAS.2001.955440
[AbstractPlus](#) | Full Text: [PDF\(664 KB\)](#) IEEE CNF
[Rights and Permissions](#)
4. **Large scale ATM switch architecture for Tbit/s systems**
 Moriwaki, N.; Makimoto, A.; Oguri, Y.; Wada, M.; Kozaki, T.;
[Global Telecommunications Conference, 1998. GLOBECOM 98, The Bridge to Global Integration,](#)
 Volume 1, 8-12 Nov. 1998 Page(s):334 - 338 vol.1
 Digital Object Identifier 10.1109/GLOCOM.1998.775751
[AbstractPlus](#) | Full Text: [PDF\(440 KB\)](#) IEEE CNF
[Rights and Permissions](#)
5. **Making parallel packet switches practical**
 Iyer, S.; McKeown, N.;
[INFOCOM 2001, Twentieth Annual Joint Conference of the IEEE Computer and Communications Proceedings, IEEE](#)
 Volume 3, 22-26 April 2001 Page(s):1680 - 1687 vol.3
 Digital Object Identifier 10.1109/INFCOM.2001.916665
[AbstractPlus](#) | Full Text: [PDF\(72 KB\)](#) IEEE CNF
[Rights and Permissions](#)

6. **Soft switching space vector PWM inverter using a new quasi-parallel resonant DC link**
Jung, Y.c.; Liu, H.L.; Cho, G.C.; Cho, G.H.;
[Power Electronics, IEEE Transactions on](#)
Volume 11, [Issue 3](#), May 1996 Page(s):503 - 511
Digital Object Identifier 10.1109/63.491645
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(684 KB\)](#) IEEE JNL
[Rights and Permissions](#)
7. **Formal verification of memory circuits by switch-level simulation**
Bryant, R.E.;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on](#)
Volume 10, [Issue 1](#), Jan. 1991 Page(s):94 - 102
Digital Object Identifier 10.1109/43.62795
[AbstractPlus](#) | Full Text: [PDF\(832 KB\)](#) IEEE JNL
[Rights and Permissions](#)
8. **Reconfigurable buses with shift switching: concepts and applications**
Lin, R.; Olariu, S.;
[Parallel and Distributed Systems, IEEE Transactions on](#)
Volume 6, [Issue 1](#), Jan. 1995 Page(s):93 - 102
Digital Object Identifier 10.1109/71.363407
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(752 KB\)](#) IEEE JNL
[Rights and Permissions](#)
9. **Efficient parallel time-slot interchanger for high-performance SDH/SONET digital crossconn**
Obara, H.;
[Electronics Letters](#)
Volume 37, [Issue 2](#), 18 Jan 2001 Page(s):81 - 83
Digital Object Identifier 10.1049/el:20010079
[AbstractPlus](#) | Full Text: [PDF\(328 KB\)](#) IET JNL
10. **Soft switching space vector PWM inverter using a new quasi-parallel resonant DC link**
Jung, Y.C.; Liu, H.L.; Cho, G.C.; Cho, G.H.;
[Power Electronics Specialists Conference, 1995. PESC '95 Record, 26th Annual IEEE](#)
Volume 2, 18-22 June 1995 Page(s):936 - 942 vol.2
Digital Object Identifier 10.1109/PESC.1995.474928
[AbstractPlus](#) | Full Text: [PDF\(368 KB\)](#) IEEE CNF
[Rights and Permissions](#)
11. **Scheduling unsplittable flows using parallel switches**
Mneimneh, S.; Kai-Yeung Siu;
[Communications, 2002. ICC 2002, IEEE International Conference on](#)
Volume 4, 28 April-2 May 2002 Page(s):2410 - 2415 vol.4
Digital Object Identifier 10.1109/ICC.2002.997276
[AbstractPlus](#) | Full Text: [PDF\(352 KB\)](#) IEEE CNF
[Rights and Permissions](#)
12. **Modeling compiled communication costs in multiplexed optical networks**
Salisbury, C.; Melhem, R.;
[Parallel Processing Symposium, 1997. Proceedings, 11th International](#)
1-5 April 1997 Page(s):71 - 79
Digital Object Identifier 10.1109/IPPS.1997.580850
[AbstractPlus](#) | Full Text: [PDF\(836 KB\)](#) IEEE CNF
[Rights and Permissions](#)
13. **A reliable hardware barrier synchronization scheme**
Sivaram, R.; Stunkel, C.B.; Panda, D.K.;
[Parallel Processing Symposium, 1997. Proceedings, 11th International](#)
1-5 April 1997 Page(s):274 - 280
Digital Object Identifier 10.1109/IPPS.1997.580908
[AbstractPlus](#) | Full Text: [PDF\(752 KB\)](#) IEEE CNF
[Rights and Permissions](#)

14. **Automatic reconfiguration and yield of the TESH multicomputer network**
Maziarz, B.M.; Jain, V.K.;
[Computers, IEEE Transactions on](#)
Volume 51, [Issue 8](#), Aug. 2002 Page(s):963 - 972
Digital Object Identifier 10.1109/TC.2002.1024742
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(798 KB\)](#) IEEE JNL
[Rights and Permissions](#)
15. **Routing techniques for massively parallel communication**
Felperin, S.A.; Gravano, L.; Pifarre, G.D.; Sanz, J.L.C.;
[Proceedings of the IEEE](#)
Volume 79, [Issue 4](#), April 1991 Page(s):488 - 503
Digital Object Identifier 10.1109/5.92043
[AbstractPlus](#) | Full Text: [PDF\(1680 KB\)](#) IEEE JNL
[Rights and Permissions](#)
16. **Interleaved PWM with discontinuous space-vector modulation**
Kun Xing; Lee, F.C.; Borojevic, D.; Zhihong Ye; Mazumder, S.;
[Power Electronics, IEEE Transactions on](#)
Volume 14, [Issue 5](#), Sept. 1999 Page(s):906 - 917
Digital Object Identifier 10.1109/63.788496
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(528 KB\)](#) IEEE JNL
[Rights and Permissions](#)
17. **Commutated relay switching system**
Payne, T.R.; Eichelberger, C.E.;
[Industry Applications, IEEE Transactions on](#)
Volume 25, [Issue 4](#), July-Aug. 1989 Page(s):669 - 674
Digital Object Identifier 10.1109/28.31245
[AbstractPlus](#) | Full Text: [PDF\(432 KB\)](#) IEEE JNL
[Rights and Permissions](#)
18. **A photonic knockout switch for high-speed packet networks**
Eng, K.Y.;
[Selected Areas in Communications, IEEE Journal on](#)
Volume 6, [Issue 7](#), Aug. 1988 Page(s):1107 - 1116
Digital Object Identifier 10.1109/49.7831
[AbstractPlus](#) | Full Text: [PDF\(760 KB\)](#) IEEE JNL
[Rights and Permissions](#)
19. **The bypass queue in fast packet switching**
Sarkies, K.W.;
[Communications, IEEE Transactions on](#)
Volume 39, [Issue 5](#), May 1991 Page(s):766 - 774
Digital Object Identifier 10.1109/26.87139
[AbstractPlus](#) | Full Text: [PDF\(864 KB\)](#) IEEE JNL
[Rights and Permissions](#)
20. **Broadband packet switches based on dilated interconnection networks**
Lee, T.T.; Liew, S.C.;
[Communications, IEEE Transactions on](#)
Volume 42, [Issue 234](#), Part 1, Feb-Apr 1994 Page(s):732 - 744
Digital Object Identifier 10.1109/TCOMM.1994.577102
[AbstractPlus](#) | Full Text: [PDF\(1288 KB\)](#) IEEE JNL
[Rights and Permissions](#)
21. **A three-stage ATM switch with cell-level path allocation**
Collier, M.;
[Communications, IEEE Transactions on](#)
Volume 45, [Issue 6](#), June 1997 Page(s):701 - 709
Digital Object Identifier 10.1109/26.592612
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(276 KB\)](#) IEEE JNL

[Rights and Permissions](#)**22. A note to low-power linear feedback shift registers**

Hamid, M.E.; Chien-In Henry Chen;

[Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on \[see also Circ Express Briefs, IEEE Transactions on\]](#)

Volume 45, Issue 9, Sept. 1998 Page(s):1304 - 1307

Digital Object Identifier 10.1109/82.718599

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(156 KB\)](#) IEEE JNL[Rights and Permissions](#)**23. Delta-sigma cellular automata for analog VLSI random vector generation**

Cauwenberghs, G.;

[Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on \[see also Circ Express Briefs, IEEE Transactions on\]](#)

Volume 46, Issue 3, March 1999 Page(s):240 - 250

Digital Object Identifier 10.1109/82.754858

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1116 KB\)](#) IEEE JNL[Rights and Permissions](#)**24. A time optimal wavelength rerouting algorithm for dynamic traffic in WDM networks**

Mohan, G.; Murthy, C.S.R.;

[Lightwave Technology, Journal of](#)

Volume 17, Issue 3, March 1999 Page(s):406 - 417

Digital Object Identifier 10.1109/50.749380

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(240 KB\)](#) IEEE JNL[Rights and Permissions](#)**25. Optimal all-to-all personalized exchange in self-routable multistage networks**

Yang, Y.; Wang, J.;

[Parallel and Distributed Systems, IEEE Transactions on](#)

Volume 11, Issue 3, March 2000 Page(s):261 - 274

Digital Object Identifier 10.1109/71.841742

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(384 KB\)](#) IEEE JNL[Rights and Permissions](#)

View: 1-25 |;

[Help](#) [Contact Us](#) [Privac](#)

© Copyright 2006 IE

Indexed by
 Inspec


 Search Results

[BROWSE](#)
[SEARCH](#)
[IEEE XPLORE GUIDE](#)

Results for "(((parallel<in>metadata) <and> (switching<in>metadata))<and> (timesta..."

Your search matched 2 of 1632036 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

 e-mail

» Search Options

[View Session History](#)

[New Search](#)

» Key



Indicates full text access

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

Check to search only within this results set

Display Format: Citation Citation & Abstract

[Select All](#) [Deselect All](#)

1. **A scaled version of the elastic time algorithm**
 Quaglia, F.;
[Parallel and Distributed Simulation, 2001. Proceedings. 15th Workshop on 15-18 May 2001](#) Page(s):157 - 164
 Digital Object Identifier 10.1109/PADS.2001.924632
[Abstract](#) | Full Text: [PDF](#)(684 KB) IEEE CNF
[Rights and Permissions](#)
2. **Fair scheduling of integrated IP and ATM based traffic in 3G mobile communication network**
 Chie Dou; Shing-Jeh Jiang;
[Vehicular Technology Conference, 2001. VTC 2001 Spring. IEEE VTS 53rd Volume 4, 6-9 May 2001](#) Page(s):2715 - 2719 vol.4
 Digital Object Identifier 10.1109/VETECS.2001.944094
[Abstract](#) | Full Text: [PDF](#)(428 KB) IEEE CNF
[Rights and Permissions](#)

Indexed by
 Inspec®

[Help](#) [Contact Us](#) [Privac](#)

© Copyright 2006 IE